

ABSTRACT OF THE DISCLOSURE

A signal processing circuit includes a digital-to-analog (D/A) conversion circuit having a D/A converter and a buffer for converting input digital grayscale data into an analog voltage and outputting the converted analog voltage, a first switch that changes its output mode so that a supplied voltage is output as a supply analog voltage, a second switch that outputs any of the converted analog voltage and the supply analog voltage as an analog voltage, and a detection circuit. The detection circuit determines whether or not the input digital grayscale data matches internal set data. When it is determined that the input digital grayscale data matches the set data, the detection circuit switches the first switch so that the supply analog voltage corresponding to the digital grayscale data is output, and also switches the second switch so that the supply analog voltage is output.